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<http://vlsidesignconference.org/>

Dr. Nabil I. Khachab (FULL CV) - kuniv.edu -

Analog/Digital VLSI design, (The Institute of Electrical and Electronics Engineers) Experience 1993: International Symposium on Circuits and Systems:

<http://portal.eng.kuniv.edu/cv/?f=khachab>

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IEEE Xplore Full-Text HTML : Guest Editorial -

International Symposium on VLSI Design and received the B.Tech. degree in electronics and electrical communications engineering from USA, in 1993, where

<http://ieeexplore.ieee.org/xpls/icp.jsp?arnumber=6948289>

Direct-sequence code-division multiple access - -

2012 International Symposium on Communications and (audio and video signal processing), VLSI, (The Institute of Electrical and Electronics Engineers

<http://technav.ieee.org/tag/5569/direct-sequence-code-division-multiple-a>

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<http://www.shopping.com/ieee-Vlsi-Test-Symposium-Proceedings/products>

2008 International Symposium on Vlsi, Design, -

2008 International Symposium on Vlsi, Design, Automation and Test [Institute of Electrical and Electronics Engineers] on Amazon.com. *FREE* shipping on qualifying offers.

<http://www.amazon.com/2008-International-Symposium-Design-Automation/dp/1424416167>

2016 International Symposium on VLSI Design, -

The 2016 International Symposium on VLSI Design, Automation and Test (2016 VLSI-DAT) will again be held in the Ambassador Hotel, Hsinchu, Taiwan during April 25-27, 2016.

<http://expo.itri.org.tw/2016VLSIDAT>

Kaushik Roy | Purdue University | ZoomInfo.com -

Dr. Kaushik Roy Wrong Dr. Kaushik Roy Data Systems International Inc; Mts NetApp; Member of Technical Staff NetApp; Senior Software Engineer Motorola Inc;

<http://www.zoominfo.com/p/Kaushik-Roy/1504738630>

Maher Rizkalla - Engineering and Technology IUPUI -

Solid State Electronics, VLSI Design, of the Great Lakes Symposium on VLSI, 1996. Maher E. Rizkalla and M.T of Electrical and Electronics Engineers,

<http://enr.iupui.edu/~rizkalla/>

VLSI-EDA Laboratory - Member -

IEEE Transactions on Very Large Scale Integration ACM International Symposium on Physical Design Journal of the Chinese Institute of Electrical

<http://vlsi-eda.cm.nctu.edu.tw/yumin/>

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2015 International Symposium on VLSI Design, -

The VLSI-DAT and the VLSI-TSA symposia are proud to create this annual platform for the latest technical exchanges and communications between experts from all over

<http://vlsidat.itri.org.tw/2015/General/>

ABOUT - University of Connecticut School of -

Towards a Unified Object Storage Foundation for Scalable Storage Systems, Advanced VLSI Design. IEEE - Institute of Electrical and Electronics Engineers,

<http://www.engr.uconn.edu/~cek10006/>

Past Events | IEEE Circuits and Systems Society | -

2012 International Symposium on VLSI Design, Technology Research Institute sponsored by the Institute of Electrical and Electronics Engineers

<http://ieee-cas.org/conferences/past/page/23>

Department of Electrical Engineering, Indian -

Modeling and Optimization in VLSI of electrical networks", International Journal of the Institute of Electronics and

<http://www.ee.iitb.ac.in/web/faculty/publications/hn>

Biography - University of California, Los Angeles -

driven physical design of VLSI of the Design Automation Conference in 1992, 1993 and Institute of Electrical and Electronics Engineers

http://cadlab.cs.ucla.edu/~cong/nsf_workshop99/old/bios.html.4_28_06.bak

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VLSI-DAT (Apr 2015), International Symposium on -

The International Symposium on VLSI Design, Automation and Test, organized by the VLSI Symposium will take place from 27th April to the 29th April 2015 at the The

<http://10times.com/international-symposium-on-vlsi-design>

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Anantha Chandrakasan | Massachusetts Institute of -

VLSI Design; Fellow Institute of Electrical and "said Anantha Chandrakasan, head of electrical International Symposium on Low Power Electronics and Design

<http://www.zoominfo.com/p/Anantha-Chandrakasan/12412949>

VLSI Symposia | Symposia on VLSI Technology and -

The VLSI Symposia is an international conference on semiconductor technology and circuits that offers an new initiative in the Symposium on VLSI Circuits, are

<http://www.vlsisymposium.org/>

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2014 VLSI-DAT, International Symposium on VLSI -

2013 International Symposium on VLSI Design, Automation & Test. In view of the exploding popularity of electronic consumer products and the interrelationships between

<http://vlsidat.itri.org.tw/2014/General/>

19th International Symposium VLSI Design and Test -

VDAT 2015 is 19th in the series of symposia which started in 1997 to promote R&D on all aspects of VLSI to act as a forum for free discussion on emerging topics of

<http://www.conferencealerts.com/show-event?id=142298>

Dr. SanGregory - Cedarville University -

of this work at the 6th Annual IEEE International ASIC Conference and Exhibit" in Rochester, NY in 1993. CMOS VLSI Design; 2014 Syllabus; Electrical Machines

<http://people.cedarville.edu/Employee/sangreg/>

2015 International Symposium on VLSI Technology, -

2015 International Symposium on VLSI Technology, Speakers from both design and technology field will give penetrating speeches to all the audience.

<http://vlsitsa.itri.org.tw/2015/General/>

H. Narayanan [Department of Electrical Engineering -

Transactions of the Institute of Electronics and H.Narayanan,Electrical Networks and Proceedings of 12th international conference in VLSI design,

<http://www.ee.iitb.ac.in/wiki/faculty/hn>

VLSI, 1993. Design Automation of High Performance -

VLSI, 1993. Design Automation of High Performance VLSI Systems, Proceedings. Third Great Lakes Symposium on. [Institute of Electrical and Electronics Engineers;]

<http://www.worldcat.org/title/vlsi-1993-design-automation-of-high-performance-vlsi-systems-proceedings-third-great-lakes-symposium-on/oclc/756588895>

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